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(Use several sheets if necessary)

Docket Number (Optional) Application Number POU920000088US1 Applicant(s) Chen et al. Filing Date

Herewith

				U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING IF APPRO				
Lisc	В	5321752	06/14/94	Iwamura et al.	380	24					
Loc	b	5513133	04/30/96	Cressel et al.	364	754	0,				
Loc Loc	С	5764554	06/09/98	Monier	364	746	45				
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							084 09 122				
FOREIGN PATENT DOCUMENTS											
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Trans YES	hatkon NO			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Kornerup, Peter, "A Systolic, Linear-Array Multiplier for a Class of Right-Shift Algorithms," IEEE Transacti ns on Computers, Vol. 43, No. 8, August, 1994, pp 892-898.

Montgomery, Peter, "Modular Multiplication Without Trial Division," Mathematics of Computation, Vol. 44, No.170, April 1985, pp. 519-521.

EXAMINER

DATE CONSIDERED

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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